

FIG.2

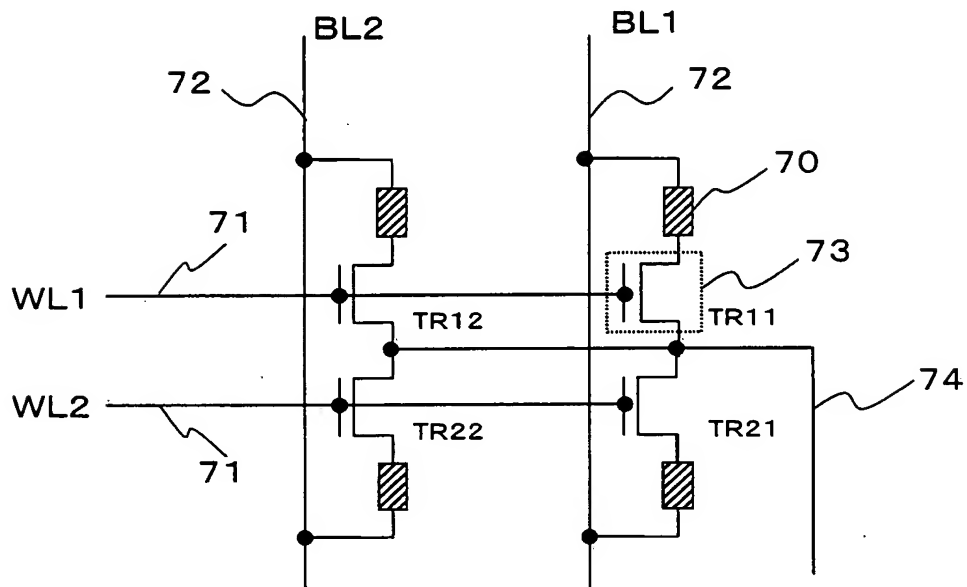


FIG.3

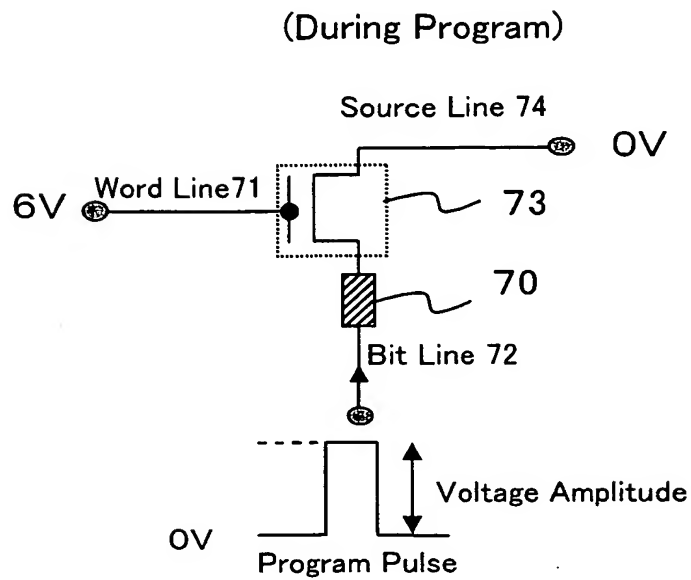


FIG.4

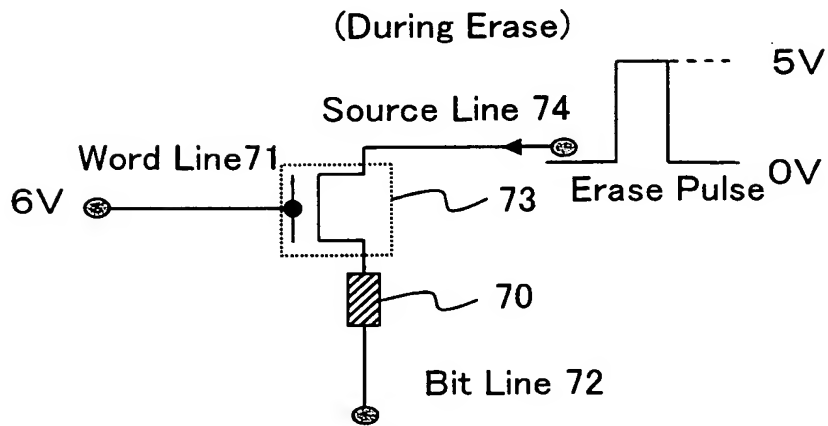


FIG.5

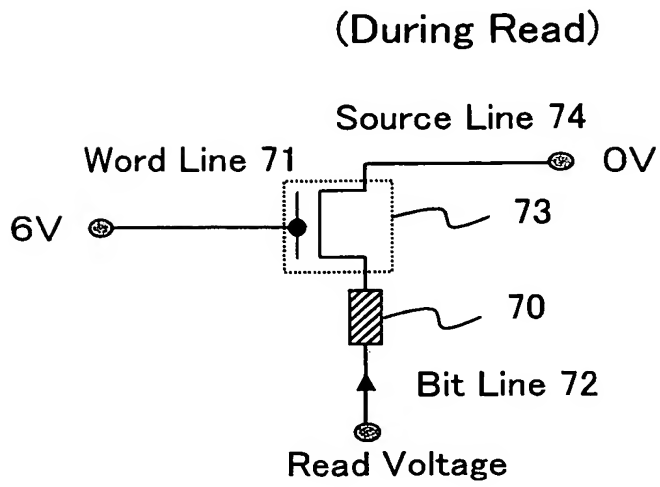


FIG.6

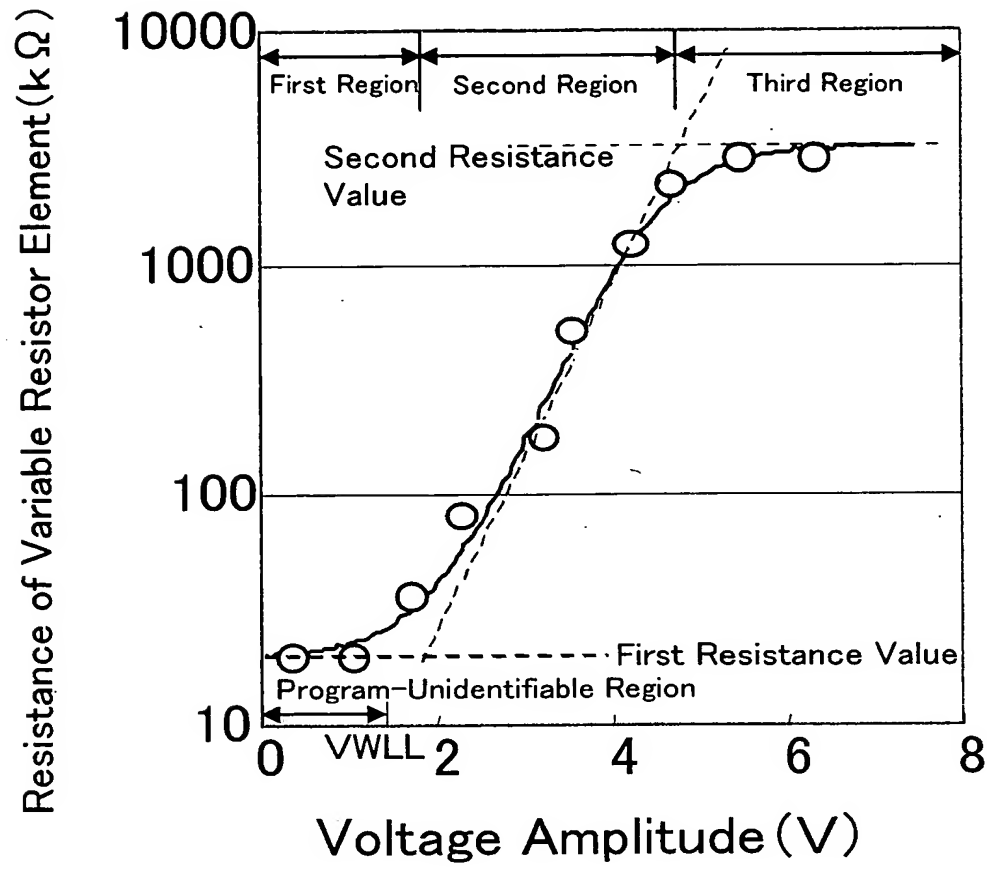


FIG.7

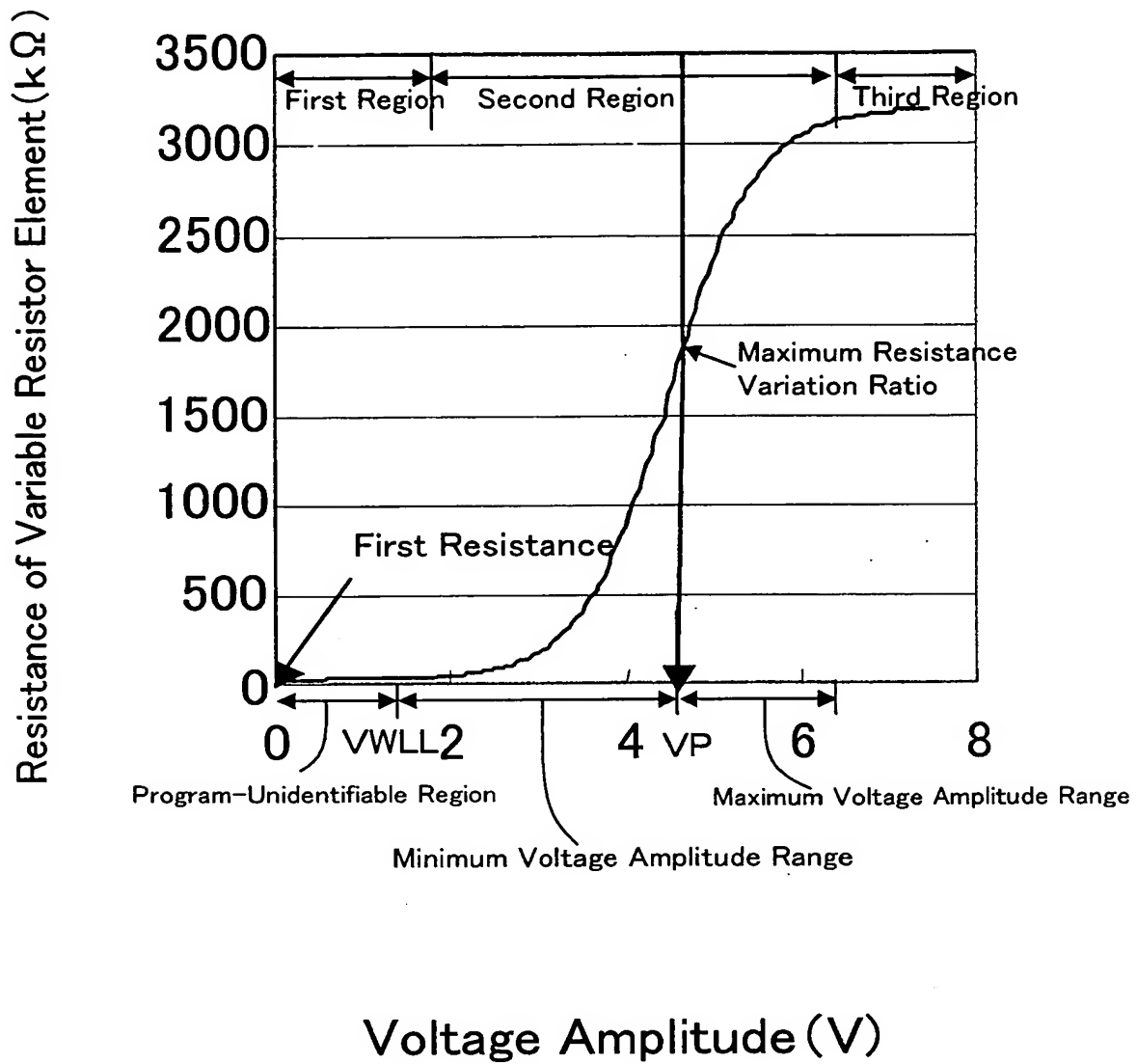
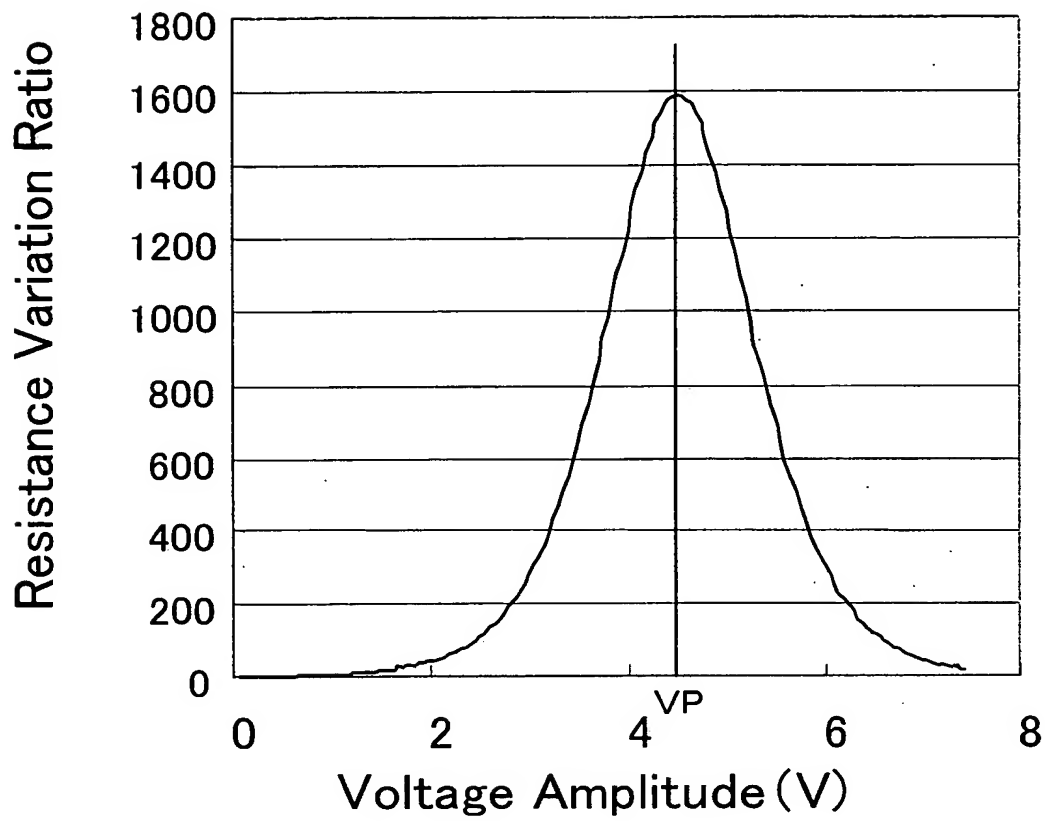


FIG.8

**FIG.9**

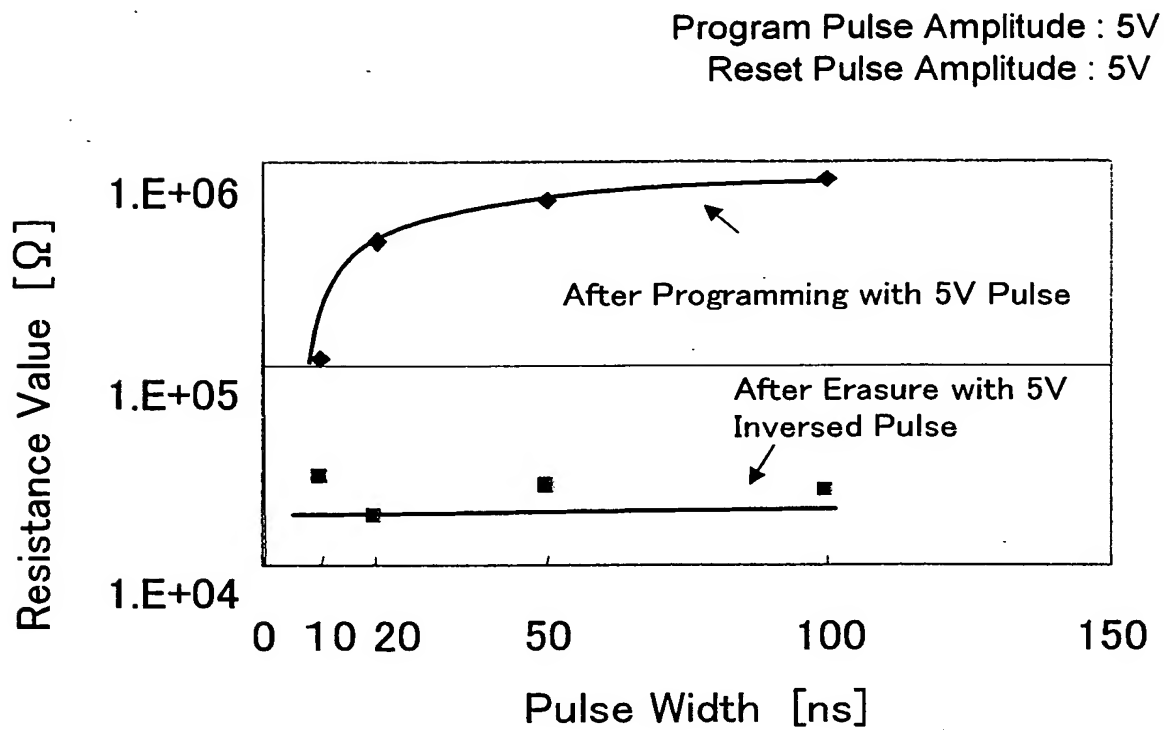


FIG.10

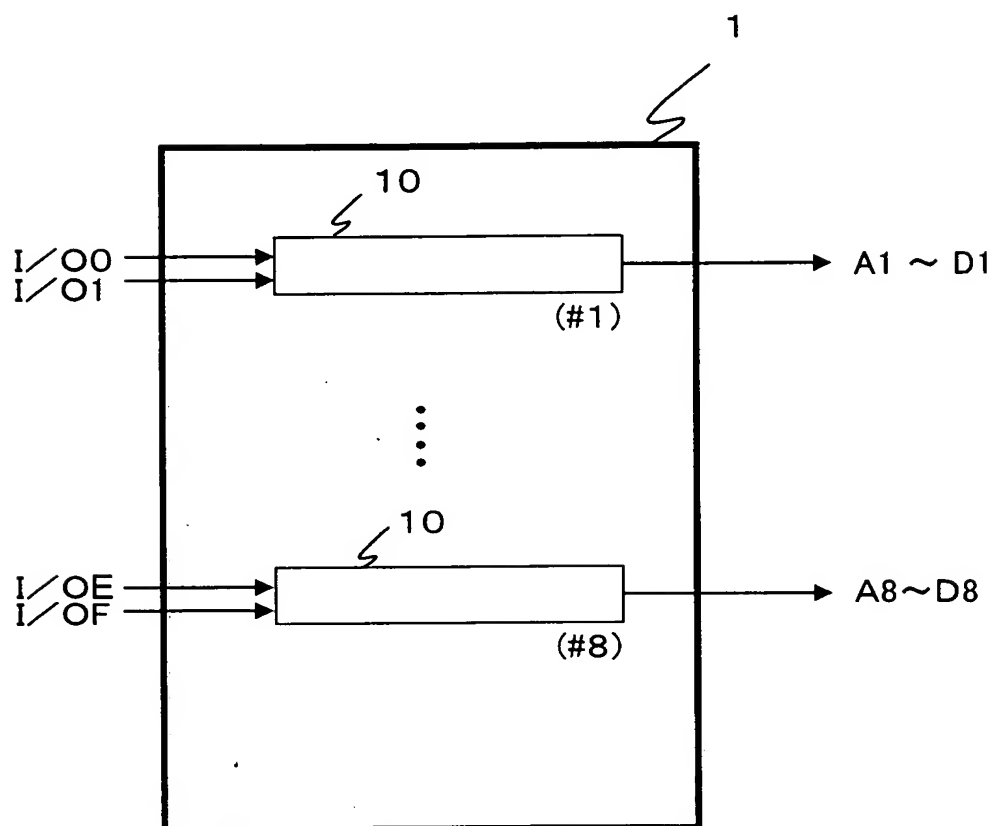


FIG.1 1

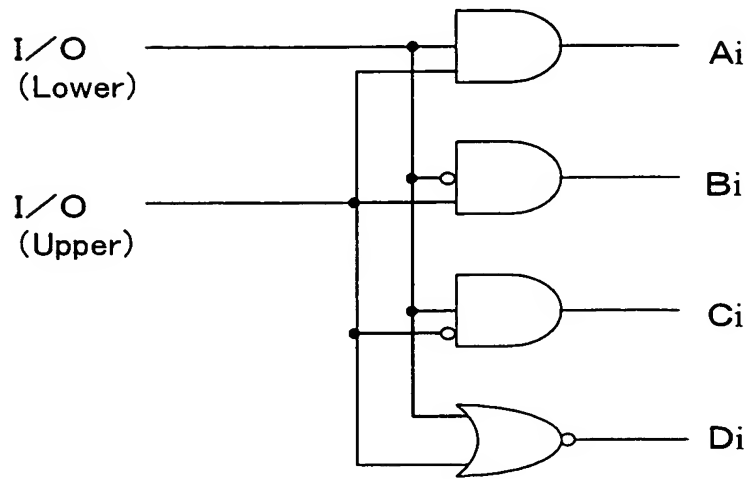


FIG. 1 2A

I/O Lower Bit	I/O Upper Bit	Ai	Bi	Ci	Di	Voltage Amplitude
1	1	1	0	0	0	W11 (5V)
1	0	0	1	0	0	W10 (4. 2V)
0	1	0	0	1	0	W01 (3. 5V)
0	0	0	0	0	1	W00 (0V)

FIG.1 2B

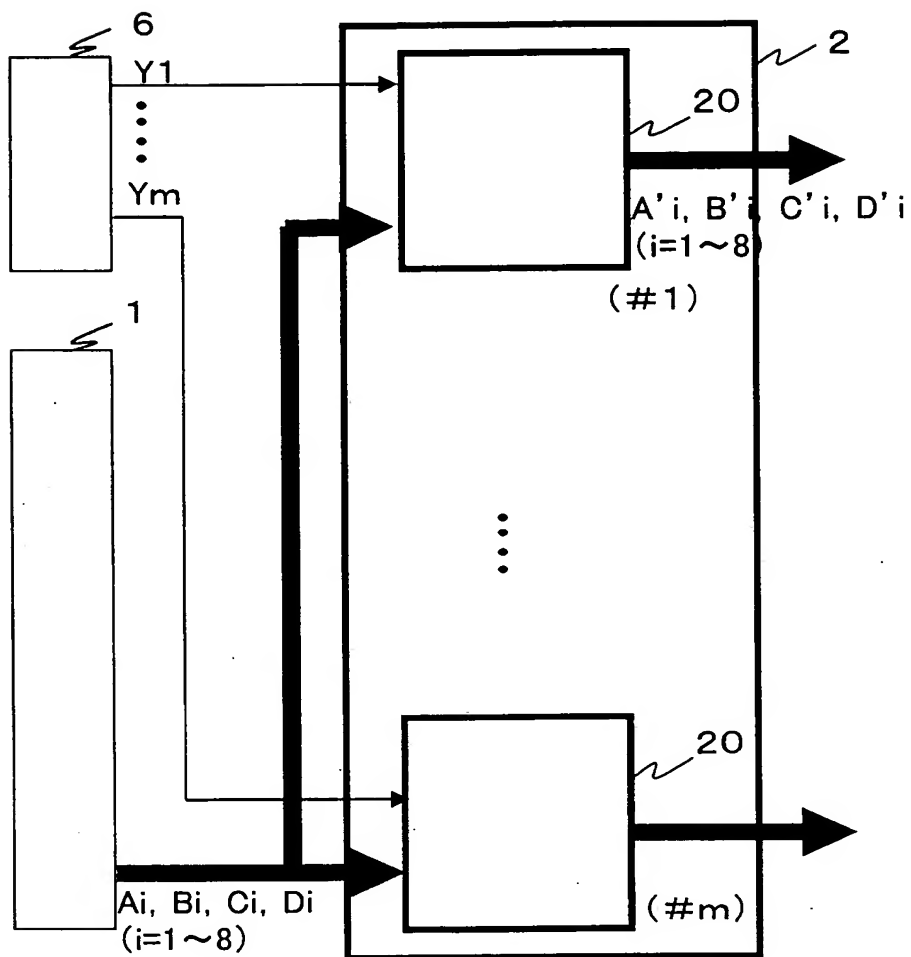


FIG.13

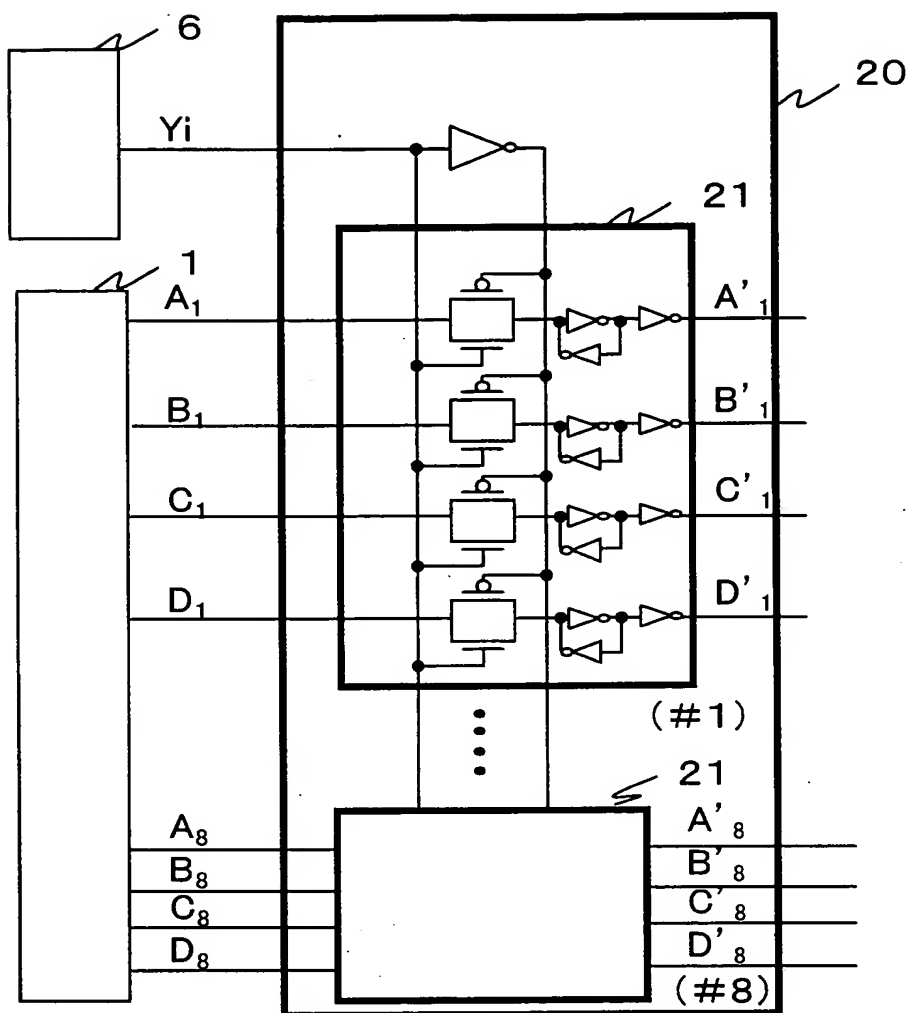
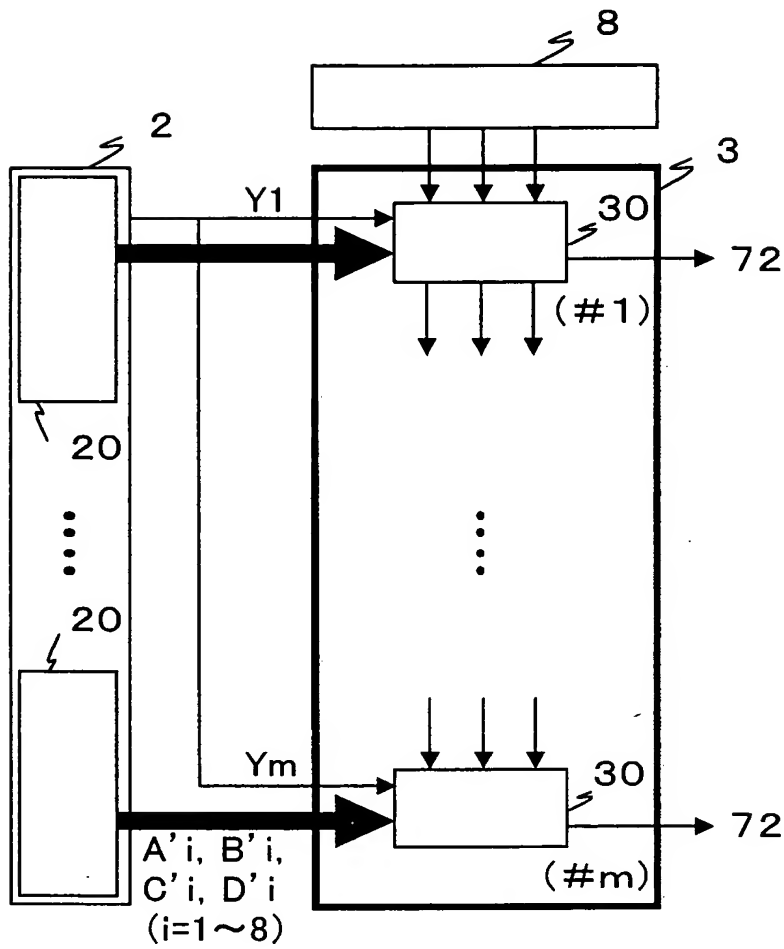
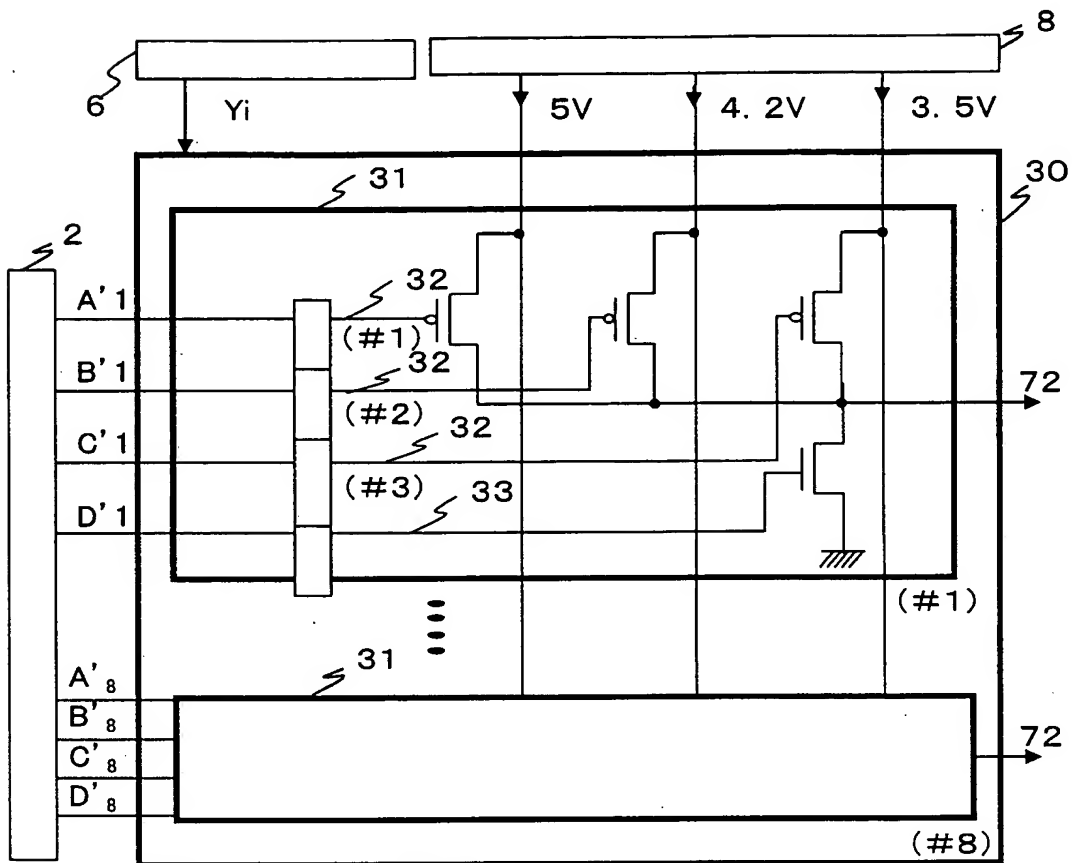


FIG. 14

**FIG. 15**



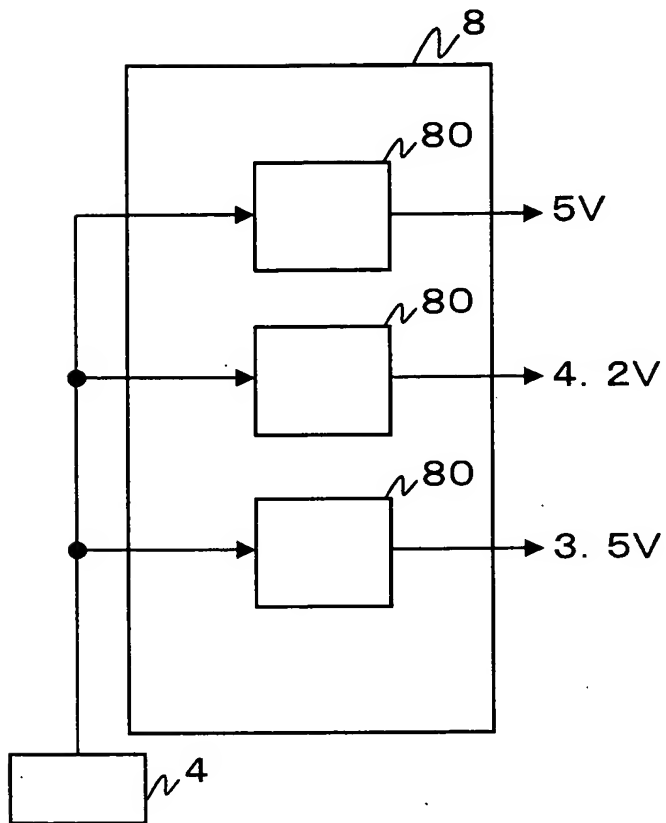


FIG. 17

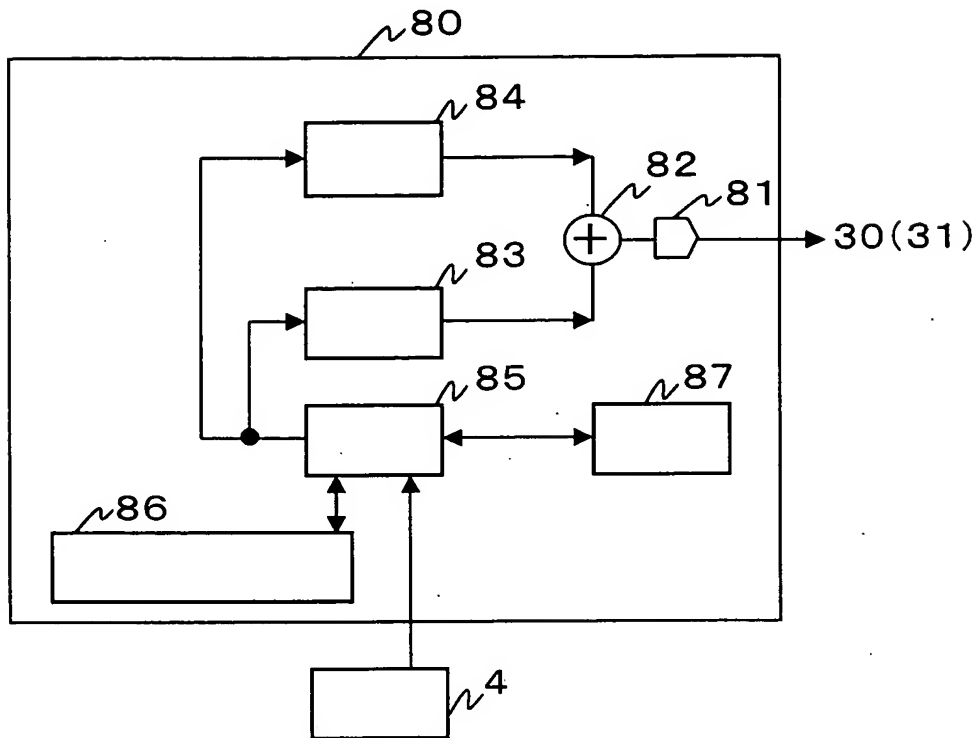


FIG. 18

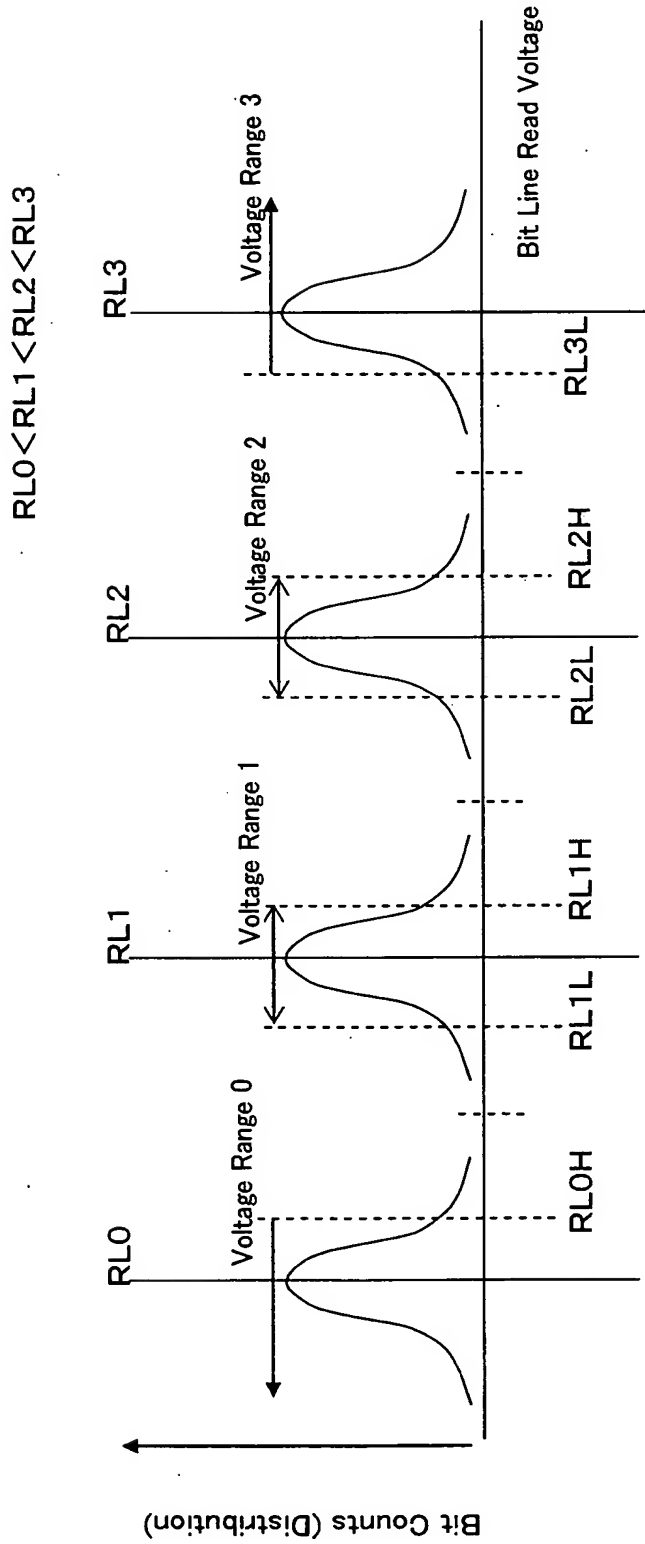


FIG. 19

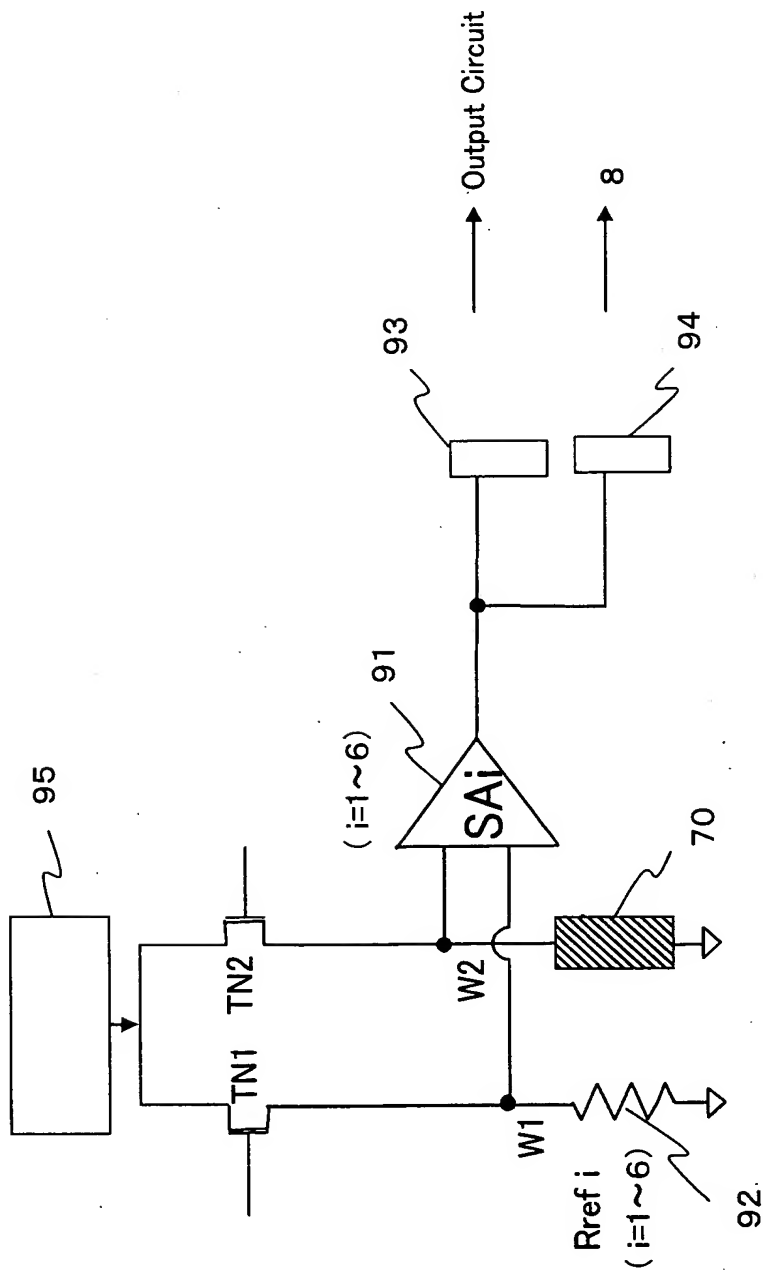


FIG.20